

PATENT
P56908**IN THE CLAIMS**

Please amend claim 24 as follows:

1. (Previously Presented) A flat panel display device, comprising:

a first substrate;

an electron emission assembly being formed on said first substrate;

a second substrate being provided at a predetermined distance from said first substrate, said first and second substrates forming a vacuum assembly; and

an illumination assembly being formed on said second substrate, said illumination assembly being illuminated by electrons emitted from said electron emission assembly;

said illumination assembly comprising:

at least one anode electrode being formed on a first surface of said second substrate to face said first substrate, the first surface of said second substrate facing said first substrate;

a plurality of phosphor layers being formed in a predetermined pattern on said at least one anode electrode; and

a plurality of conductive layers being formed on said phosphor layers, said plurality of conductive layers being made of carbon nanotubes.

2. (Canceled)

PATENT
P56908

1 3. (Previously Presented) The flat panel display device of claim 1, with said carbon
2 nanotubes having a length not longer than 5 micrometers.

1 4. (Original) The flat panel display device of claim 1, with said plurality of
2 conductive layers being formed by electrophoresis.

1 5. (Original) The flat panel display device of claim 1, with said at least one anode
2 electrode being formed in a predetermined anode electrode pattern and corresponding to a
3 plurality of anode electrodes formed at a predetermined distance on said second substrate to
4 form a striped pattern.

1 6. (Original) The flat panel display device of claim 5, further comprising:
2 a plurality of black matrix layers being formed between said plurality of anode
3 electrodes, said plurality of black matrix layers not contacting said plurality of anode
4 electrodes.

1 7. (Original) The flat panel display device of claim 6, with said plurality of black
2 matrix layers being electrically conductive and contacting said plurality of conductive layers.

1 8. (Original) The flat panel display device of claim 5, further comprising:
2 a plurality of black matrix layers being formed between said plurality of anode

PATENT
P56908

electrodes, said plurality of black matrix layers contacting said plurality of anode electrodes.

9. (Original) The flat panel display device of claim 8, with said plurality of black matrix layers being electrically conductive and contacting said plurality of conductive layers.

10. (Original) The flat panel display device of claim 1, with said at least one anode electrode including indium tin oxide.

11. (Original) The flat panel display device of claim 1, with said at least one anode electrode being formed as a single unit covering over 80% of the first surface of said second substrate.

12. (Original) The flat panel display device of claim 1, said electron emission assembly comprising:

a plurality of cathode electrodes being formed on a first surface of said first substrate to face said second substrate, the first surface of said first substrate facing said second substrate;

at least one gate electrode being formed on the first surface of said first substrate to face said second substrate;

an insulation layer separating said plurality of cathode electrodes from said at the least one gate electrode; and

PATENT
P56908

10 a plurality of electron emission sources being formed on said plurality of cathode
11 electrodes and being positioned within holes formed in said insulation layer and in said at
12 least one gate electrode.

1 13. (Original) The flat panel display device of claim 12, with said plurality of
2 electron emission sources including at least one carbon-based material selected from the
3 group consisting of carbon nanotubes, fullerenes, diamond-like carbon, graphite, and a
4 mixture of these materials.

1 14. (Previously Presented) The flat panel display device of claim 1, said electron
2 emission assembly comprising:

3 at least one gate electrode being formed on the first surface of said first substrate to
4 face said second substrate;

5 an insulation layer being formed on the gate electrode;

6 a plurality of cathode electrodes being formed on the insulation layer; and

7 a plurality of electron emission sources being mounted on said plurality of cathode
8 electrodes.

1 15. (Original) The flat panel display device of claim 14, with said plurality of
2 electron emission sources including at least one carbon-based material selected from the
3 group consisting of carbon nanotubes, fullerenes, diamond-like carbon, graphite, and a

PATENT
P56908

4 mixture of these materials.

1 16 - 20. (Canceled)

1 21. (Previously Presented) A flat panel display device, comprising:

2 a first substrate;

3 an electron emission assembly being formed on said first substrate;

4 a second substrate being provided at a predetermined distance from said first
5 substrate, said first and second substrates forming a vacuum assembly;

6 an illumination assembly being formed on said second substrate, said illumination
7 assembly being illuminated by electrons emitted from said electron emission assembly, said
8 illumination assembly comprising at least one anode electrode being formed on a first
9 surface of said second substrate to face said first substrate, the first surface of said second
10 substrate facing said first substrate, a plurality of phosphor layers being formed in a
11 predetermined pattern on said at least one anode electrode, and a plurality of conductive
12 layers being formed on said phosphor layers, said plurality of conductive layers being formed
13 of a carbon-based material; and

14 a plurality of black matrix layers being formed between said plurality of anode
15 electrodes, said plurality of black matrix layers contacting said plurality of conductive
16 layers.

PATENT
P56908

1 22. (Previously Presented) The flat panel display device of claim 21, said plurality
2 of conductive layers being made of carbon nanotubes.

1 23. (Previously Presented) The flat panel display device of claim 21, said plurality
2 of black matrix layers contacting said plurality of anode electrodes.

1 24. (Currently Amended) The flat panel display device of claim 21, said plurality of
2 black matrix layers being electrically conductive.